

The Performance Of Erasure Codes Used In FT-MPI

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ABSTRACT: *Today, the scale of High performance computing (HPC) systems is much larger than ever. Some HPC systems consist of thousands or even tens of thousands of processors. The larger scale leads to a challenge that how to deal with process failures. The most important programming tool for HPC is MPI (Message Passing Interface). There are some existing methods to deal with fault-tolerance, such as MPICH-V, StarFish, MPI/FT and so on, using the MPI context. Most of them do the checkpoint on disk. In this paper, some erasure codes, which used in RAID systems usually, are applied to deal with the fault-tolerance in-memory. Based on fault-tolerance-MPI (FT-MPI) platform, RAID4, RAID5, RDP and X-Code are implanted to do the checkpoint in-memory. The experimental results show that RDP is feasible for double-fault-tolerance in-memory.*

KEYWORDS: *MPI; fault-tolerance; RDP; X-Code*

I. INTRODUCTION

Today, the scale of High performance computing (HPC) systems is much larger than ever. Some systems consist of thousands or even tens of thousands of processors. According to the data from Top500 in June, 2003, most systems (54.8%) include 129-512 processors. But in November, 2007, most systems (53.6%) includes 1025-2048 processors. The fastest system in 2007 consists of more than twenty thousands processors^[1].

A challenge to the systems which include so many processors is the ability to deal with hardware failures. Concluding from the current experiences on the top-end machines, a 100,000-processor machine will experience a process failure every few minutes. In order to solve this problem, some fault tolerance mechanisms must be considered in both hardware and software design.

The most important programming tool for HPC is MPI (Message Passing Interface). Combining MPI and fault tolerance, users can improve the reliability of applications. Usually, this MPI is called fault-tolerance-MPI (FT-MPI). There are three kinds of fault-tolerance implementation for MPI. Firstly, fault-tolerance mechanism can be implemented by global checkpoint. After failure occurring, the MPI applications are restored to recent checkpoint. Secondly, fault-tolerance mechanism also can be implemented based on event-log. At last, fault-tolerance mechanism can be implemented using replications.

Cocheck is a fault-tolerance MPI based on checkpoint^[2]. StarFish, another fault-tolerance MPI, is similar to Cocheck^[3]. Cocheck is developed using Codor, but StarFish is developed on a distributed system.

Log-based fault-tolerance MPI has two log Strategies, one is optimistic log, the other is pessimistic log. MPI-FT^[4] is a fault-tolerance mechanism implemented on LAM-MPI^[5]. MPI-FT can use optimistic or pessimistic log according to user's requirements. MPICH-V2^[6] use Checkpoints Scheduler to maintain consistence.

In this paper, some error-correcting codes, which used in RAID systems usually, are applied to deal with the fault-tolerance in-memory. Some typical construction methods of single and double erasure codes, such as RAD4, RAID5, Row-Diagonal Parity (RDP)^[7] and X-code^[8], are implemented on FT-MPI.

II. ARCHITECTURE OF FT-MPI

There are three main parts in FT_MPI, detection, notification and recovery. The architecture of FT-MPI is shown in figure 1.

FT-MPI system is built in a hierarchical structure. MPI Library layer is the API to applications. The second layer is responsible for data transfer. FT-MPI runtime library (FTRTL) is responsible for interaction with operation system through Harness library. FTRTL can receive failure notification from both communication library and Harness library. According to the source of communications, FTMPI can determine the reason of failure.

III. FAULT-TOLERANCE IN-MEMORY USING ERASURE CODES

Based on the FTMPI platform, some erasure codes are used to implement fault-tolerance mechanism in-memory.

A. Single erasure codes

In this paper, RAID 4 and RAID 5 are used as single erasure codes for fault-tolerance in-memory.

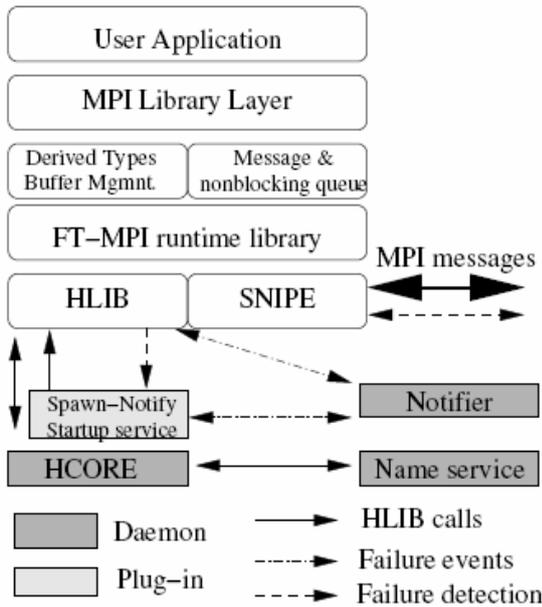


Figure 1. architecture of FT-MPI

As shown in figure 2, all parity data are stored in a single disk. Using the idea of RAID 4, a process can be considered a disk figure 2.

	Disk0	Disk 1	Disk 2	Disk 3	Disk 4
0	D0	D1	D2	D3	p0-3
1	D4	D5	D6	D7	p4-7
2	D8	D9	D10	D11	p8-11
3	D12	D13	D14	D15	p12-15
4	D16	D17	D18	D19	p16-19

Figure 2. Data layout of RAID 4

Further more, the last process play a role of redundancy process like parity disk in figure 2. The processes are shown in figure 3. And the data recovery process is shown in figure 4.

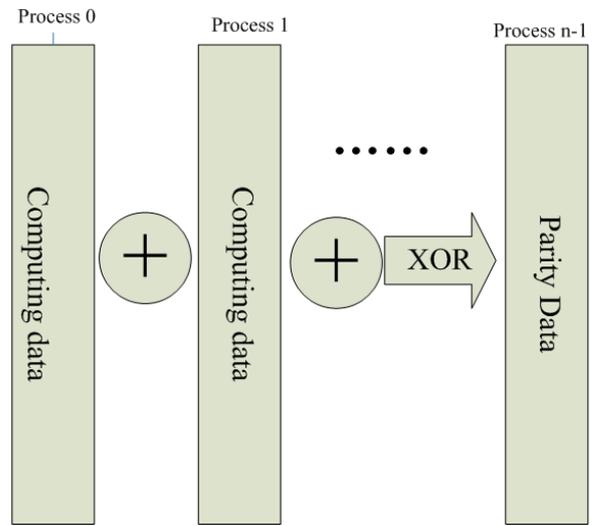


Figure 3. RAID 4 in FT-MPI

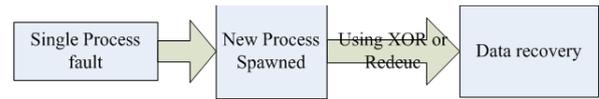


Figure 4. the data recovery of RAID 4

According to RAID 5 data layout, as shown in figure 5, the parity data are distributed to all disks. This data layout can improve system performance effectively.

	Disk0	Disk 1	Disk 2	Disk 3	Disk 4
0	D0	D1	D2	D3	p0-3
1	D5	D6	D7	p4-7	D4
2	D10	D11	p8-11	D8	D9
3	D15	p12-15	D12	D13	D14
4	p16-19	D16	D17	D18	D19

Figure 5. the data layout of RAID 5

Applying RAID 5 to FTMPI, the data in process is divided into stripes like the stripes in RAID 5. Because all the jobs are done in memory, the integrity of data is not destroyed.

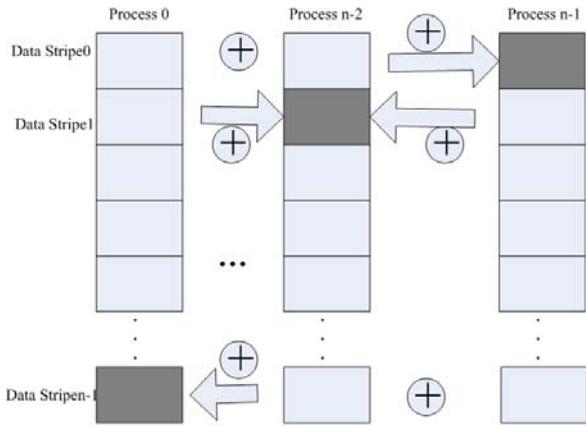


Figure 6. RAID 5 in FTMPI

The data recovery process is shown in figure 7.

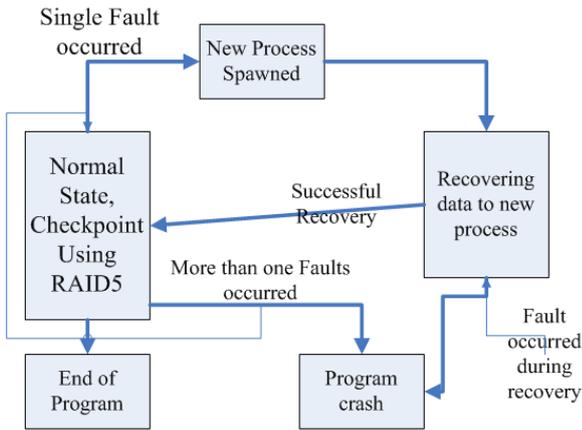


Figure 7. the data recovery of RAID 5

B. Double erasure codes

RAID 4 and RAID 5 can only deal with single fault events. In order to handle double faults, some double erasure codes are implemented in FTMPI.

RDP is a kind of double erasure code. The parity data is gotten only using XOR operation. The data layout of RDP with 4 data disks is shown in figure 8.

DataDisk 0	DataDisk 1	DataDisk 2	DataDisk 3	Row Parity	Diag. Parity
0	1	2	3	4	0
1	2	3	4	0	1
2	3	4	0	1	2
3	4	0	1	2	3

Figure 8. data layout of RDP (4 data disks)

The implementation of RDP in FTMPI is shown in figure 9.

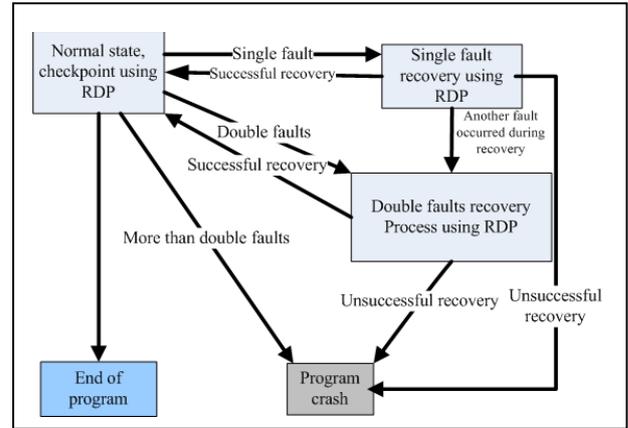


Figure 9. The data recovery of RDP

X-Code is another double erasure code used in FT-MPI. In X-Code, all data units are stored in a $(n-2) * n$ matrix. The parity units are stored in a $2 * n$ matrix. They construct a $n * n$ matrix.

Considering as the data in the i th line and j th column, the parity of X-Code can be described as follows,

$$C_{n-2,i} = \sum_{k=0}^{n-3} C_{k, <i+k+2>_n} \quad i=0,1,\dots,v-1 \quad (1)$$

$$C_{n-1,i} = \sum_{k=0}^{n-3} C_{k, <i-k-2>_n} \quad i=0,1,\dots,v-1 \quad (2)$$

The data recovery process of X-Code is shown in figure 10.

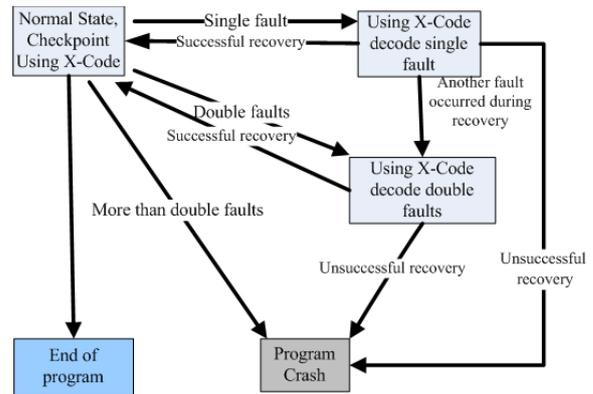


Figure 10. The data recovery of X-code

IV. EXPERIMENTAL RESULTS

The experiments to FT-MPI are run on some PC which connected by Gigabit Ethernet. The operation system is Redhat AS 4.0. The program with checkpoint is PCG, which is an iterative algorithm for linear systems.

A. The cost of checkpoint

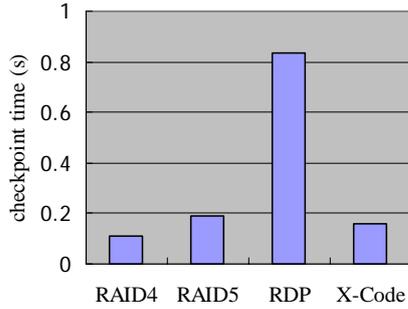


Figure 11. the cost of checkpoint

As shown in figure 11, the checkpoint time of RDP is higher than other codes obviously. The reason is RDP need more XOR calculation than others.

B. The runtime under single fault

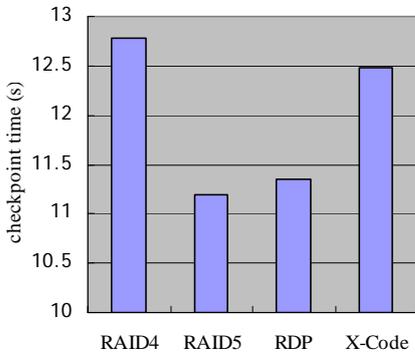


Figure 12. the runtime under single fault

Figure 12 shows the runtime of different erasure codes under single fault. The runtime includes executive time, checkpoint time and recovery time. As shown in figure 12, RDP and RAID5 have shorter runtime.

C. The runtime under double faults

Because only RDP and X-Code support double-fault-tolerance in FT-MPI, Figure 13 shows their runtime under double faults. According to the results, RDP is more effective than X-Code.

V. CONCLUSIONS

With the development of HPC, HPC systems need more and more nodes and processor. The larger scale will lead to the decline in system reliability. FT-MPI can tolerate hardware or software faults on program level. In this paper, some erasure codes are used in FT-MPI to improve program reliability. The experimental results show that RDP is feasible for double-fault-tolerance. In the future works, the erasure codes for more than double faults will be tried in FT-MPI.

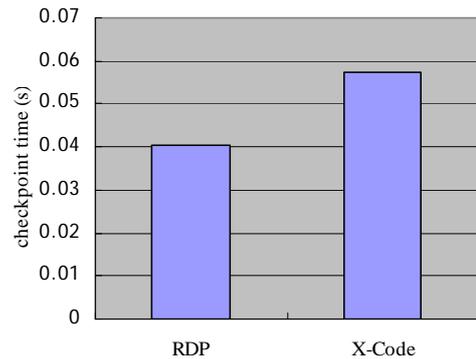


Figure 13. the runtime under double faults

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